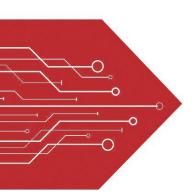
# MSKSEMI















**ESD** 

**TVS** 

**TSS** 

MOV

**GDT** 

**PLED** 

Broduct data speet



#### Features

- ♦ 30W (8/20µs) Peak Pulse Power
- ♦ Low Capacitance ESD Protection
- ♦ SOD-882 Package
- ♦ RoHS Compliant
- ♦ Matte Tin Lead finish (Pb-Free)
- ♦ Protect One High Speed Data Line
- ♦ Meet IEC61000-4-2 Level 4: Contact Discharge > 8kV Air Discharge > 15kV

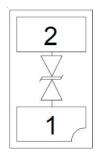
### **Applications**

- ♦ Communication System
- ♦ Portable Instrumentation
- ♦ Audio and Video Equipment
- ♦ Computers and Peripherals
- ♦ USB 1.1, USB 2.0 Ports

## Circuit Diagram



PIN Diagram



SOD-882

# Electrical Characteristics ( $Ta = 25^{\circ}C$ )

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
VRWM	Reverse Working Peak Voltage				5	\
VBR	Reverse Breakdown Voltage	IT = 1mA	5.5		9.5	V
IR	Reverse Leakage Current	VRWM = 5V			0.1	μA
VC	Clamping Voltage	IPP = 1A (8/20μs)			12	V
VC	Clamping Voltage	IPP = 2A (8/20μs)			15	V
CJ	Capacitance	VR = 0V, f = 1MHz		3.0	3.5	ρF

# Maximum Ratings (Ta = $25^{\circ}$ C)

Symbol	Parameter	Value	Unit
РРК	Peak Pulse Power	30	W
IPP	Peak Pulse Current	2	А
VESD (Contact)	Contact ESD Voltage per IEC61000-4-2	8	kV
VESD (Air)	Air ESD Voltage per IEC61000-4-2	15	kV
TJ	Junction Temperature	-55 to +150	°C
TSTG	Storage Temperature	-55 to +150	°C

## **Electrical Parameter**

Symbol	Parameter
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
V <sub>RWM</sub>	Working Peak Reverse Voltage
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>
I <sub>T</sub>	Test Current
$V_{BR}$	Breakdown Voltage @ I <sub>T</sub>

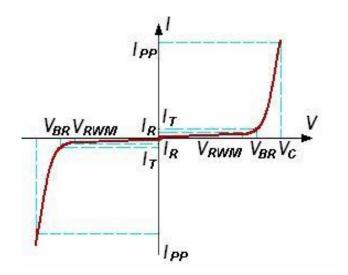


FIG1: Pulse Waveform

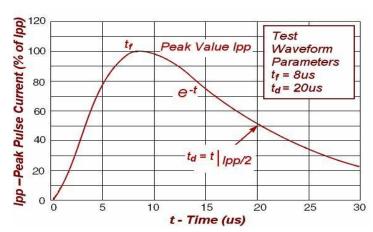
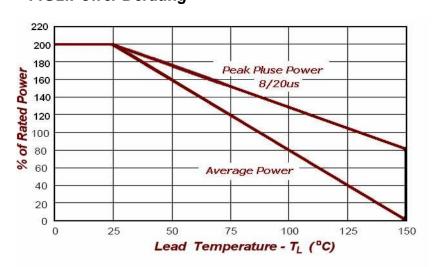
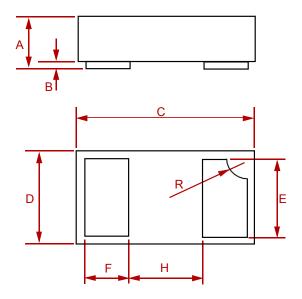


FIG2:Power Derating



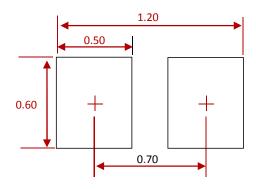


#### **PACKAGE MECHANICAL DATA**



Direc	Inches		Millimeters		
Dim	MIN	MAX	MIN	MAX	
Α	0.0125	0.02	0.32	0.52	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
E	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
Н	0.015Typ.		0.40Typ.		
R	0.001	0.005	0.05	0.15	

# **Suggested Pad Layout**



#### NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

#### **REEL SPECIFICATION**

P/N	PKG	QTY
ESD5V0U1UL-MS	SOD-882	10000



## **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.