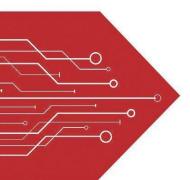
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Broduct data sheet



Semiconductor

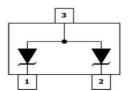
Compiance

Features

- ♦ 80Watts peak pulse power (tp = 8/20µs)
- ◆ Transient protection for high speed data lines to IEC 61000-4-2 (ESD) ±30kV (air), ±30kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns)
- ♦ Working voltages :3V,5V,12V,15V,24V
- Protects two bidirectional line
- ♦ Low operating and clamping voltages
- ◆ Solid-state silicon avalanche technology

Applications

- ♦ Notebooks, Desktops, Servers and Video Graphics Cards
- ◆ USB Power & Data Line Protection
- Monitors and Flat Panel Displays
- ♦ I²C Bus Protection
- Portable Instrumentation
- ♦ Set Top Box





SOT-23

Electrical Characteristics@ Ta=25°C unless otherwise

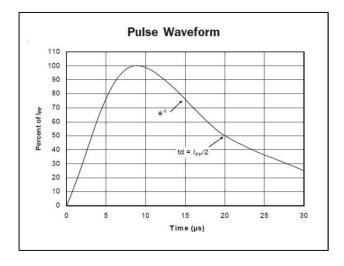
	V_{RWM}	V _B	Ι _Τ	Vc@1A	٧	′c	I _R	Ст
P/N	(V)	(V)	(mA)	(V)	(\	/)	(µA)	(pF)
	(max.)	(min.)		(max.)	(max.)	(@A)	(max.)	(max.)
ESD3V3U1UT-MS	3.3	4	1	7.0	14	5	40	1.5
ESD5V0U1UT-MS	5	6	1	9.8	18	5	10	1.5
ESD12VU1UT-MS	12	13.3	1	19	32	5	1	1.5
ESD15VU1UT-MS	15	16.7	1	24	38	5	1	1.5
ESD24VU1UT-MS	24	26.7	1	43	52	3	1	1.5

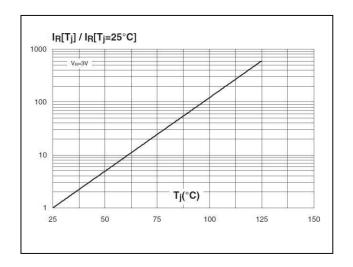
Maximum Rating @ Ta=25 °C unless otherwisespecified

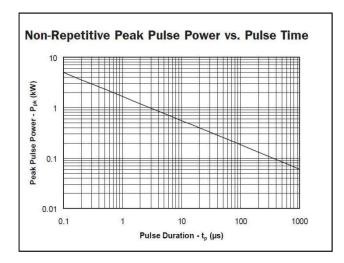
Symbol	Parameter	Ratings	Units
P _{PK}	Peak Pulse Power (tp = 8/20μs)	80	Watts
TL	Lead Soldering Temperature	260(10sec.)	$^{\circ}\!\mathbb{C}$
TJ	Operating Temperature	-55 to +125	${\mathbb C}$
T _{STG}	Storage Temperature		$^{\circ}\!\mathbb{C}$

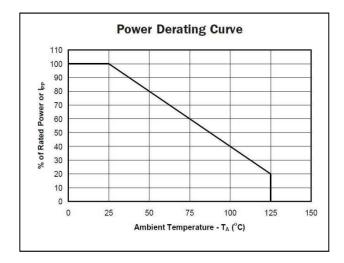


Typical Characteristics@ Ta=25°C unless otherwise specified







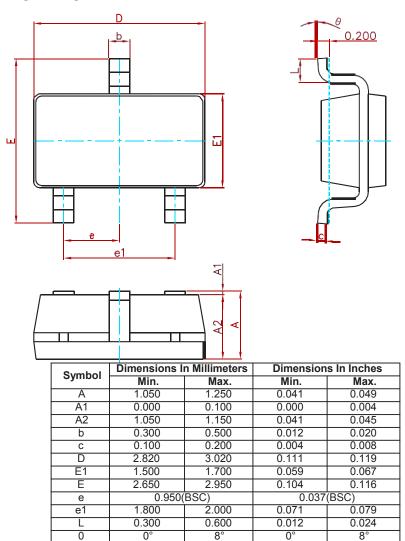




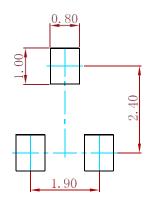
Semiconductor

Complance

PACKAGE MECHANICAL DATA



Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
ESDXXVU1UT-MS	SOT-23	3000



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