



Product data sheet

www.msksemi.com







TO-252

FEATURES

Maximum output current I_{OM} : 0.5 A Output voltage V_0 : 6V Continuous total dissipation P_D : 1.25 W

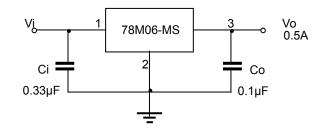
ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Unit
Input Voltage	Vi	25	V
Operating Junction Temperature Range	T _{OPR}	0-+125	°C
Storage Temperature Range	T _{STG}	-65-+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (Vi=11V, IO=350mA,Ci=0.33µF,Co=0.1µF, unless otherwise specified)

Parameter	Symbol	Test conditions		Min	Тур	Max	Unit
			25℃	5.75	6	6.25	V
Output Voltage	Vo	8V≤V i≤21V, Io=5mA-350mA Po≤ 15W	0-125℃	5.7	6	6.3	V
Load Regulation	ΔVo	lo=5mA-0.5A	25℃		18	120	mV
		lo=5mA-200mA	25℃		10	60	mV
Line Regulation	ΔVo	8V≤V _i ≤25V, lo=200mA	25℃		5	100	mV
		9V≤V _i ≤25V, lo=200mA	25℃		1.5	50	mV
Quiescent Current	lq		25℃		4.3	6	mA
Quiescent Current Change	Δlq	9V≤V _i ≤25V, Io=200mA	0-125℃			0.8	mA
Quiescent Current Change	Δlq	5mA≤I ₀ ≤350mA	0-125℃			0.5	mA
Output Noise Voltage	V _N	10Hz≤ f ≤100KHz	25℃		45		uV
Ripple Rejection	RR	9V≤V _i ≤19V,f=120Hz,Io=300mA	0-125℃	59	80		dB
Dropout Voltage	Vd	lo=350mA	25℃		2		V
Short Circuit Current	lsc	Vi=11V	25℃		270		mA
Peak Current	lpk		25℃		0.5		Α

TYPICAL APPLICATION







8.6 8.8 9.0

V_{IN}=11V T_J=25℃

Pulsed

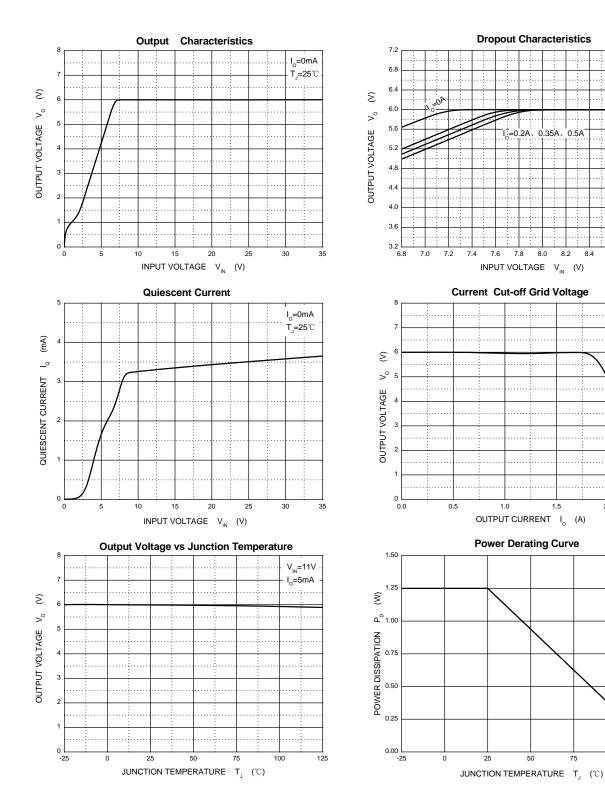
2.0

100

125

2.5

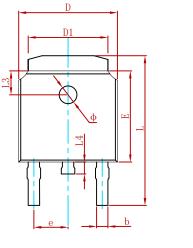
T_=25℃

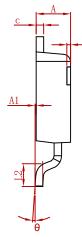




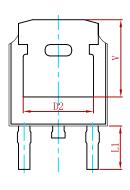


PACKAGE MECHANICAL DATA



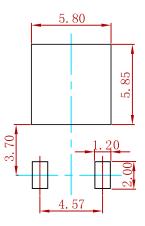


h



Symbol Dimensions In Millimeters		In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 REF.		0.190 REF.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900 REF.		0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250 REF.		0.207 REF.		

Suggested Pad Layout



Note:

1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.

3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
78M06-MS	TO-252	2500
78M06-MS	TO-252	2500





<u>Attention</u>

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications f any and all MSKSEMI Semiconductor products described orcontained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuits for safedesign, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.