MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

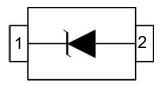
Broduct data sheet

PACKAGE OUTLINE



SOD-523

PIN CONFIGURATION



FEATURES

- ♦ IEC61000-4-2 Level 4 ESD Protection
- ♦ Protects one directional I/O line
- ♦ Low clamping voltage
- ♦ Working voltages : 36V
- ♦ Low leakage current

MACHANICAL DATA

- ♦ SOD-523 package
- ♦ Packaging: Tape and Reel
- ♦ High temperature soldering guaranted:260 °C/10s
- ♦ Reel size: 7 inch

APPLICATIONS

- ♦ Microprocessor based equipment
- ♦ Personal Digital Assistants (PDA's)
- ♦ Notebooks, Desktops, and Servers
- ♦ Portable Instrumentation
- ♦ Peripherals
- ♦ LED backlight

ABSOLUTE MAXIMUM RATING					
Symbol	Parameter	Value	Unit		
W	ESD per IEC 61000-4-2 (Air)	±25	kV		
V_{ESD}	ESD per IEC 61000-4-2 (Contact)	±20	ΚV		
P _D	Total Power Dissipation on FR-5 Board (Note 1) @ Ta=25°C	150	mW		
T _{OPT}	Operating Temperature	-55~125	°C		
T_{STG}	Storage Temperature	-55~150	°C		

These ratings are limiting values above which the serviceability of the diode may be impaired.

Note 1: FR-5=1.0x0.75x0.62 in.

ELECTRICAL CHARACTERISTICS (Tamb=25°C)							
Symbol	Parameter	Test Condition	Min	Тур	Max	Units	
V_{RWM}	Reverse Working Voltage				36	V	
V_{BR}	Reverse Breakdown Voltage	I _T = 1mA	40			V	
I _R	Reverse Leakage Current	V _{RWM} = 36V			5	μA	
V _C	Clamping Voltage	$I_{PP} = 1A, t_p = 8/20 \mu s$			55	V	
Сл	Junction Capacitance	$V_R = 0V$, $f = 1MHz$			28	pF	

ELECTRICAL CHARACTERISTICS CURVE

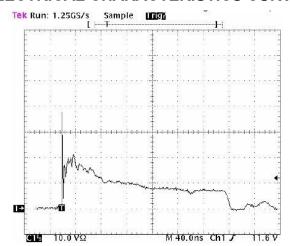
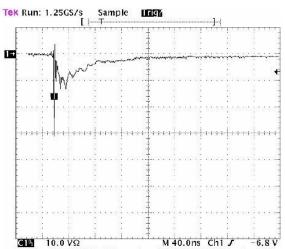


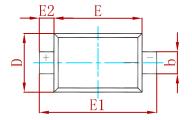
Figure 1. ESD Clamping Voltage Screenshot Positive 8 kV contact per IEC 61000-4-2

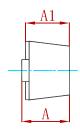


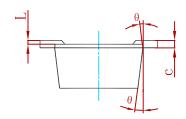
10.0 VΩ M 40.0ns Ch1 7 -6.8 V
Figure 2. ESD Clamping Voltage Screenshot
Negative 8 kV contact per IEC 61000-4-2



PACKAGE MECHANICAL DATA

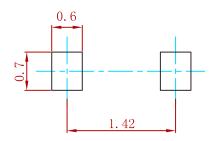






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Зуппоп	Min	Max	Min	Max	
Α	0.510	0.770	0.020	0.031	
A1	0.500	0.700	0.020	0.028	
b	0.250	0.350	0.010	0.014	
С	0.080	0.150	0.003	0.006	
D	0.750	0.850	0.030	0.033	
E	1.100	1.300	0.043	0.051	
E1	1.500	1.700	0.059	0.067	
E2	0.200 REF		0.008	REF	
L	0.010	0.070	0.001	0.003	
θ	7° I	REF	7° F	REF	

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
ESD5Z36V-MS	SOD-523	3000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described orcontained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.