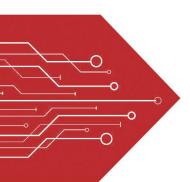
MSKSEMI















ESD

TVS

TSS

MOV

GDT

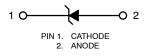
PLED

Broduct data sheet









SOD-523

Specification Features:

- Ultra Low Capacitance 0.5 pF
- Low Clamping Voltage
- Small Body Outline Dimensions: 0.047" x 0.032" (1.20 mm x 0.80 mm)
- Low Body Height: 0.024" (0.6 mm)
- Stand-off Voltage: 5 V
- Low Leakage
- Response Time is Typically < 1.0 ns
- IEC61000-4-2 Level 4 ESD Protection
- This is a Pb-Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Contact Air		±10 ±15	kV
Total Power Dissipation on FR-5 Board (Note 1) @ T _A = 25°C	P _D	200	mW
Storage Temperature Range	T _{stg}	-55 to +150	°C
Junction Temperature Range	TJ	-55 to +125	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic

Epoxy Meets UL 94 V-0

LEAD FINISH: 100% Matte Sn (Tin)

QUALIFIED MAX REFLOW TEMPERATURE: $260^{\circ}C$

Device Meets MSL 1 Requirements

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted, $V_F = 1.0 \text{ V Max.}$ @ $I_F = 10 \text{ mA}$ for all types)

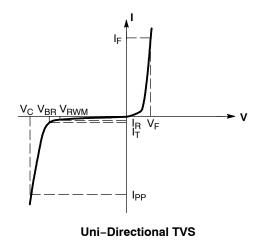
P/N	V _{RWM} (V)	Ι _R (μΑ) @ V _{RWM}	V _{BR} (V) @ I _T (Note 2)	Ι _Τ	C	(pF)	V _C (V) @ I _{PP} = 1 A (Note 3)	v _c
	Max	Max	Min	mA	Тур	Max	Max	Per IEC61000-4-2 (Note 4)
ESD8351XV2T1G-MS	3.3	1.0	5.4	1.0	0.5	0.9	9.8	Figures 1 and 2 See Below



ELECTRICAL CHARACTERISTICS

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Symbol	Parameter		
I _{PP}	Maximum Reverse Peak Pulse Current		
V _C	Clamping Voltage @ I _{PP}		
V_{RWM}	Working Peak Reverse Voltage		
I _R	Maximum Reverse Leakage Current @ V _{RWM}		
V_{BR}	Breakdown Voltage @ I _T		
I _T	Test Current		
I _F	Forward Current		
V _F	Forward Voltage @ I _F		
P _{pk}	Peak Power Dissipation		
С	Capacitance @ V _R = 0 and f = 1.0 MHz		



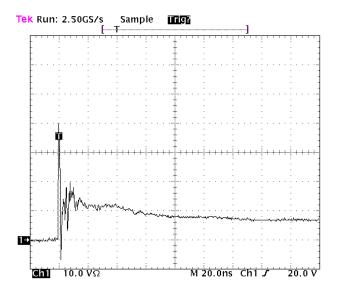


Figure 1. ESD Clamping Voltage Screenshot Positive 8 kV Contact per IEC61000-4-2

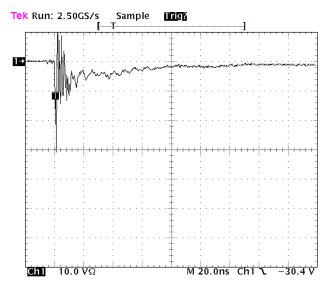


Figure 2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000-4-2

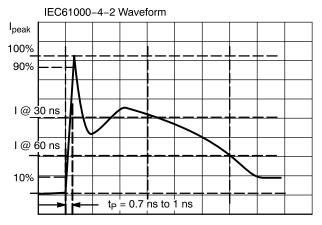


Figure 3. IEC61000-4-2 Spec

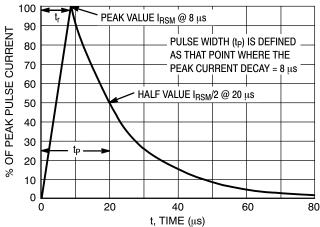


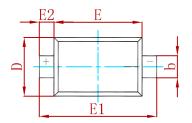
Figure 5 8 X 20 µs Pulse Waveform

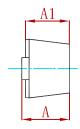


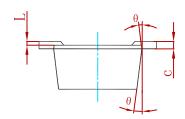
Semiconductor Compiance



PACKAGE MECHANICAL DATA

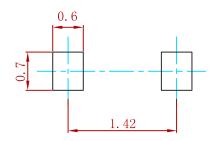






Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Зупівої	Min	Max	Min	Max	
Α	0.510	0.770	0.020	0.031	
A1	0.500	0.700	0.020	0.028	
b	0.250	0.350	0.010	0.014	
С	0.080	0.150	0.003	0.006	
D	0.750	0.850	0.030	0.033	
E	1.100	1.300	0.043	0.051	
E1	1.500	1.700	0.059	0.067	
E2	0.200 REF		0.008	REF	
L	0.010	0.070	0.001	0.003	
θ	7° REF		7° F	REF	

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
ESD8351XV2T1G-MS	SOD-523	3000



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