

MSKSEMI

SEMICONDUCTOR



ESD



TVS



TSS



MOV

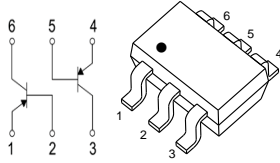


GDT



PLED

Product data sheet



SOT-363

DUAL TRANSISTOR (PNP+PNP)

FEATURES

- Two transistors in one package
- Reduces number of components and board space
- No mutual interference between the transistors

MARKING: 5Ft

MAXIMUM RATINGS (T_a=25°C unless otherwise noted)

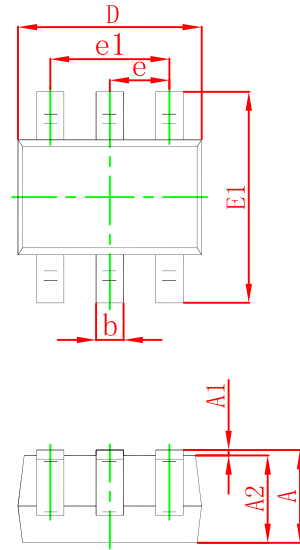
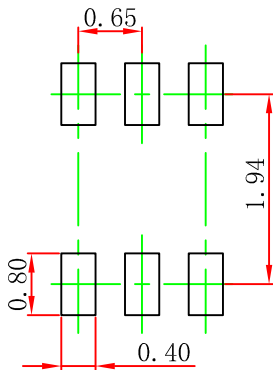
Symbol	Parameter	Value	Units
V _{CB0}	Collector- Base Voltage	-80	V
V _{CEO}	Collector-Emitter Voltage	-65	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current -Continuous	-0.1	A
P _C	Collector Power Dissipation	0.2	W
R _{θJA}	Thermal Resistance from Junction to Ambient	625	°C/W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55-150	°C

ELECTRICAL CHARACTERISTICS PNP 5401 (T_a=25°C unless otherwise specified)

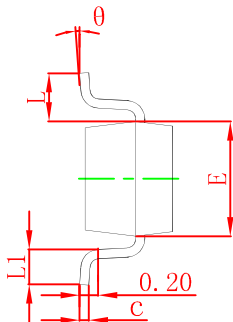
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =-10μA, I _E =0	-80			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =-10mA, I _B =0	-65			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =-10μA, I _C =0	-5			V
Collector cut-off current	I _{CB0}	V _{CB} =-30V, I _E =0			-15	nA
Emitter cut-off current	I _{EBO}	V _{EB} =-5V, I _C =0			-100	nA
DC current gain	h _{FE}	V _{CE} =-5V, I _C =-2mA	110		600	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =-10mA, I _B =-0.5mA			-0.1	V
		I _C =-100mA, I _B =-5mA *			-0.3	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C =-10mA, I _B =-0.5mA		0.7		V
Output Capacitance	C _{obo}	V _{CB} =-10V, f= 1MHz, I _E = 0			2.5	pF
Current Gain-Bandwidth Product	f _T	V _{CE} =-5V, I _C =-10mA, f= 100MHz	100			MHz

*pulse test: PW≤350μS, δ≤2%.

SOT-363



- Note:
1. Controlling dimension: in millimeters.
 2. General tolerance: $\pm 0.05\text{mm}$.
 3. The pad layout is for reference purposes only.



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

REEL SPECIFICATION

P/N	PKG	QTY
BC856S	SOT-363	3000

Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringement of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.