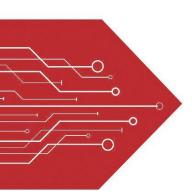
# MSKSEMI















**ESD** 

**TVS** 

**TSS** 

MOV

**GDT** 

**PLED** 

Broduct data speet



# Wice Head Fe

#### **Feature**

80W peak pulse power per line ( $t_P = 8/20\mu s$ )

Replacement for MLV(0402)

Bidirectional configurations

Response time is typically < 1ns

Low clamping voltage

RoHS compliant

Transient protection for data lines to

IEC61000-4-2(ESD) ±30KV(air), ±30KV(contact);

IEC61000-4-4 (EFT) 40A (5/50ns)

#### **Mechanical Characteristics**

Lead finish:100% matte Sn(Tin)

Mounting position: Any

Qualified max reflow temperature:260 ℃

Device meets MSL 2 requirements

Pure tin plating: 7 ~ 17 um

Pin flatness:≤3mil

## **Applications**

Cellular phones

Portable devices

Digital cameras

Power supplies

# $\longrightarrow$



DFN1006P2X

# Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	Vrwm				8	V
Breakdown Voltage	VBR	It= 1mA	9.0	11.0	13.0	V
Reverse Leakage Current	lr	V <sub>RWM</sub> = 5V T=25°C			1.0	μA
Maximum Reverse Peak Pulse	<b>I</b> PP			5.0		А
Clamping Voltage	Vc	Ipp=1A			13	V
Clamping Voltage	Vc	Ipp=3A			15	V
Clamping Voltage	Vc	Ipp=5A			17	V
Junction Capacitance	Cj	V <sub>R</sub> =0V f = 1MHz		13	15	pF

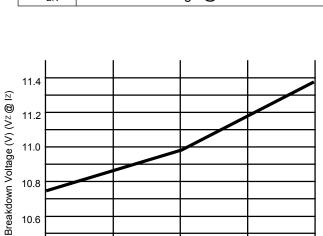
# Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t <sub>p</sub> =8/20μs)	P <sub>pp</sub>	80	W
Operating Temperature	TJ	-55 to +150	$^{\circ}\!\mathrm{C}$
Storage Temperature	T <sub>STG</sub>	-55 to +150	$^{\circ}\!\mathrm{C}$

10.4

### **Electrical Parameter**

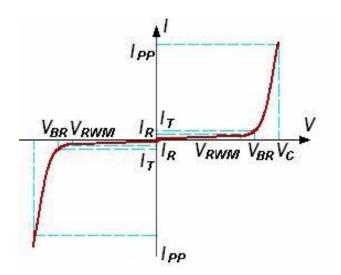
Symbol	Parameter
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
V <sub>RWM</sub>	Working Peak Reverse Voltage
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>
I <sub>T</sub>	Test Current
$V_{BR}$	Breakdown Voltage @ I⊤



Temperature (°C)
Fig .Typical Breakdown Voltage vs. Temperature

+25

+150



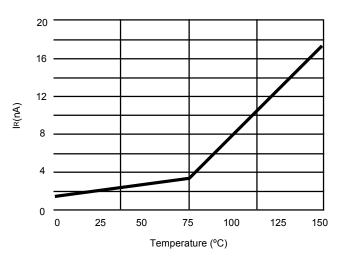


Fig . Typical Leakage Current vs. Temperature

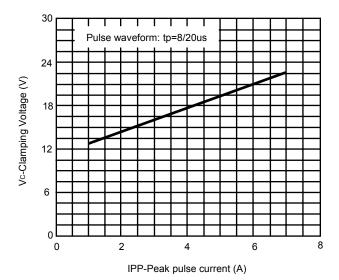
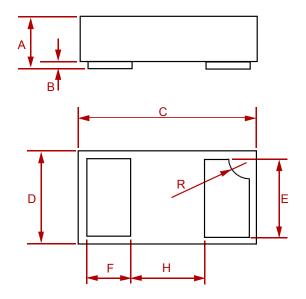


Fig Clamping voltage vs. Peak pulse current



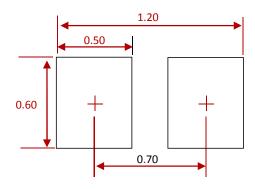
Semiconductor Compiance

### **PACKAGE MECHANICAL DATA**



Dim	Inches		Millimeters		
	MIN	MAX	MIN	MAX	
Α	0.0125	0.02	0.32	0.52	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
E	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
Н	0.015Typ.		0.40Typ.		
R	0.001	0.005	0.05	0.15	

# **Suggested Pad Layout**



#### NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

### **REEL SPECIFICATION**

P/N	PKG	QTY
AZ4208-01F-MS	DFN1006P2X	12000



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