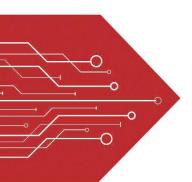
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Product data sheet



Description

The STU417S uses advanced trench technology

to provide excellent $R_{\text{DS}(\text{ON})}\text{, low gate charge and}$

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

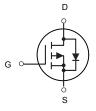


TO-252

General Features

 $V_{DS} = -40V I_{D} = -40A$

 $R_{DS(ON)}$ < 21 m Ω @ V_{GS} =10V



P-Channel MOSFET

Application

Battery protection

Load switch

Uninterruptible power supply

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-40	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	-40	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	-25	А
IDM	Pulsed Drain Current ²	-144	А
P _D @T _C =25°C	Total Power Dissipation⁴	30	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient ¹	62	°C/W
R₀JC	Thermal Resistance Junction-Case ¹	2.5	°C/W



Electrical Characteristics (T_J =25 $^{\circ}$ C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units	
Off Characteristics							
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =-250 μ A	-40			V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =-40V			-1	μ Α	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = \pm 20V, V_{DS} =0A			±100	nA	
On Characteristics	On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =-250 μ A	-1.1	-1.7	-2.5	V	
R _{DS(ON)}	Drain-Source On Resistance	V _{GS} =-10V,I _D =-20A		15	21		
		V _{GS} =-4.5V,I _D =-15A		21	32	m Ω	
G _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-5A	15			S	
Dynamic Characteristics							
C _{iss}	Input Capacitance			2050			
C _{oss}	Output Capacitance	V _{DS} =-20V, V _{GS} =0V, f=1MHz		260		pF	
C _{rss}	Reverse Transfer Capacitance			150			
Switching Characteris	Switching Characteristics						
t _{d(on)}	Turn-On Delay Time			10		ns	
t _r	Rise Time	V_{DS} =-20V, R_L =1.6 Ω		24		ns	
t _{d(off)}	Turn-Off Delay Time	R_{GEN} =3 Ω , V_{GS} =-10V		40		ns	
t _f	Fall Time			9		ns	
$\mathbf{Q}_{\mathbf{g}}$	Total Gate Charge			45		nC	
\mathbf{Q}_{gs}	Gate-Source Charge	V _{GS} =-10V, V _{DS} =-20V,		6		nC	
\mathbf{Q}_{gd}	Gate-Drain "Miller" Charge	I _D =-8A		11		nC	
Drain-Source Diode Characteristics							
V _{SD}	Source-Drain Diode Forward Voltage	V _{GS} =0V,I _S =-10A,			-1.2	V	
I _{SD}	Source-Drain Current(Body Diode)				-36	Α	

Notes:

^{1.}Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Characteristics

Figure 1. Power Dissipation

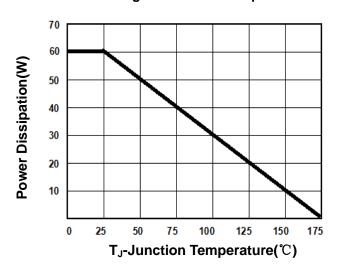


Figure 3. Output Characteristics

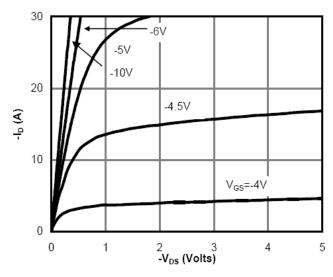


Figure 5. Capacitance

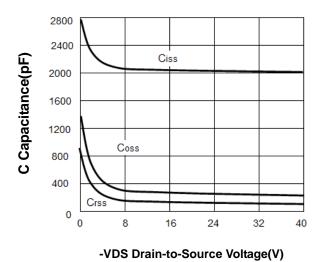


Figure2. Drain Current

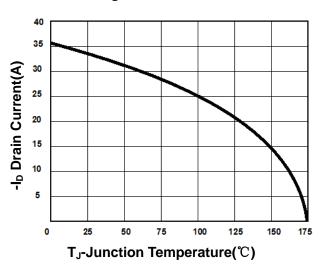


Figure 4. Transfer Characteristics

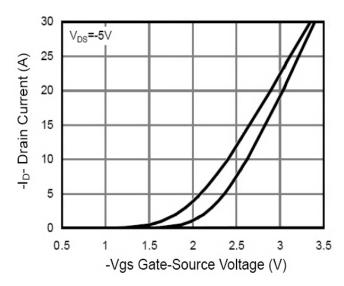


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

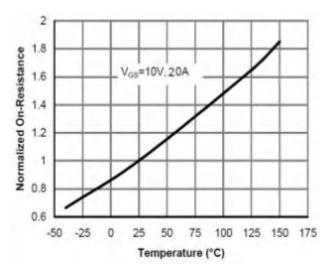




Figure 7. V_{GS(th)} vs Junction Temperature

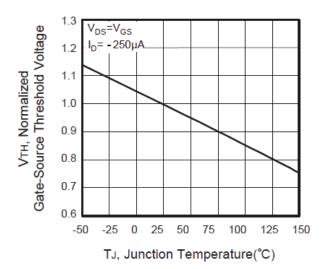


Figure8. Gate Charge Waveforms

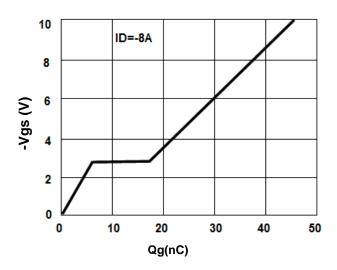
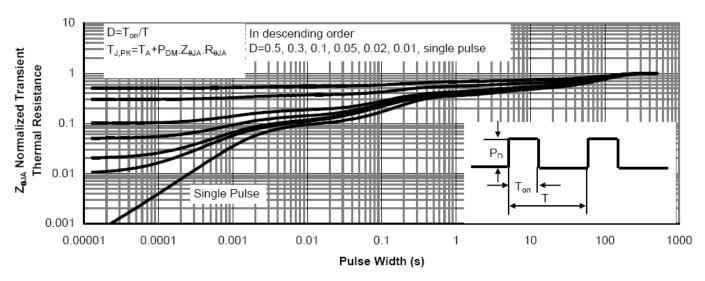
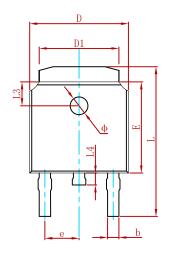


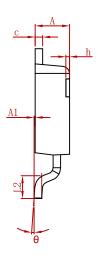
Figure 9. Normalized Maximum Transient Thermal Impedance

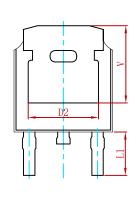




PACKAGE MECHANICAL DATA

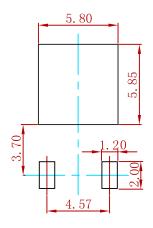






Cumbal	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
Е	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900	REF.	0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600	REF.	0.063	REF.
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250	REF.	0.207	REF.

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
STU417S	TO-252	2500



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